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NOTICE OF ALLOWANCE AND FEE(S) DUE

35273 7590 12/15/2008

BEVER, HOFFMAN & HARMS, LLP
2099 GATEWAY PLACE
SUITE 320
SAN JOSE, CA 95110

EXAMINER

CHEN, QING

ART UNIT

PAPER NUMBER

2191

DATE MAILED: 12/15/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/020,420	12/13/2001	Vidyasagar Edara	INS-120	9550

TITLE OF INVENTION: SYSTEM AND METHOD OF UTILIZING A HARDWARE COMPONENT TO EXECUTE AN INTERPRETIVE LANGUAGE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. **PROSECUTION ON THE MERITS IS CLOSED.** THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN **THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE** OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. **THIS STATUTORY PERIOD CANNOT BE EXTENDED.** SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail **Mail Stop ISSUE FEE**
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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

35273 7590 12/15/2008
BEVER, HOFFMAN & HARMS, LLP
2099 GATEWAY PLACE
SUITE 320
SAN JOSE, CA 95110

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/020,420 12/13/2001 Vidyasagar Edara INS-120 9550

TITLE OF INVENTION: SYSTEM AND METHOD OF UTILIZING A HARDWARE COMPONENT TO EXECUTE AN INTERPRETIVE LANGUAGE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHEN, QING	2191	717-139000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a **Customer Number is required.**

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, 1 _____
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 2 _____
 3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

- ☐ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)

- ☐ A check is enclosed.
☐ Payment by credit card. Form PTO-2038 is attached.
☐ The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____ Date _____
 Typed or printed name _____ Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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35273	7590	12/15/2008	EXAMINER	
BEVER, HOFFMAN & HARMS, LLP 2099 GATEWAY PLACE SUITE 320 SAN JOSE, CA 95110			CHEN, QING	
			ART UNIT	PAPER NUMBER
			2191	
DATE MAILED: 12/15/2008				

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 1238 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 1238 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability

Application No.

10/020,420

Examiner

Qing Chen

Applicant(s)

EDARA ET AL.

Art Unit

2191

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed on August 25, 2008.
2. ☒ The allowed claim(s) is/are 84-92, 94-96 and 98, renumbered as 1-13.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

DETAILED ACTION

1. This Office action is in response to the amendment filed on August 25, 2008.
2. **Claims 84-92, 94-96, and 98** are pending.
3. **Claims 89, 94-96, and 98** have been amended.
4. **Claims 1-83, 93, and 97** have been canceled.
5. **Claims 84-92, 94-96, and 98** are allowed, renumbered as 1-13.
6. The objections to Claims 4, 13, and 70 are withdrawn in view of Applicant's cancellation of the claims.
7. The 35 U.S.C. § 112, second paragraph, rejections of Claims 3 and 4 are withdrawn in view of Applicant's cancellation of the claims.

Examiner's Amendment

8. An Examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to Applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this Examiner's amendment was given in a telephone interview with Jeanette S. Harms (Reg. No. 35,537) on December 4, 2008.

The application has been amended as follows:

AMENDMENTS TO THE CLAIMS

Please cancel Claims 93 and 97 and amend Claims 89, 94-96, and 98 as follows:

89. (Currently Amended) A hardware component that facilitates executing an interpretive language in a system, the system including processing component and a memory component, wherein the hardware component provides an interface between the processing component and the memory component, the hardware component comprising:

a first multiplexer for receiving an address from the processing component and providing an output to the memory component;

an interpreter language program counter for providing inputs of the first multiplexer; ~~and~~
a decoding component for:

receiving the address,

comparing the received address to stored addresses, the stored addresses including

a fixed instruction fetch address and a plurality of fixed operand fetch addresses, and

controlling the output of the first multiplexer based on a result of the comparing;

a second multiplexer for receiving data from the memory component and providing an output to the processing component;

an instruction jump address generator component for receiving the data and providing inputs to the second multiplexer;

an operand storing component for receiving the data, storing any operands of the data, and providing inputs to the second multiplexer,

wherein the decoding component controls the second multiplexer based on the result of the comparing; and

a counter component for receiving an input from the decoding component and providing outputs to the second multiplexer and the decoding component.

93. (Canceled)

94. (Currently Amended) The hardware component of Claim [[93]]89, wherein the decoding component sets the second multiplexer to provide the data as the output of the second multiplexer when the received address fails to match any stored address.

95. (Currently Amended) The hardware component of Claim [[93]]89, wherein the decoding component sets the second multiplexer to provide an output of the instruction jump address generator component as the output of the second multiplexer when the received address matches the fixed instruction fetch address.

96. (Currently Amended) The hardware component of Claim [[93]]89, wherein the decoding component sets the second multiplexer to provide an output of the operand storing component as the output of the second multiplexer when the received address matches one of the plurality of fixed operand fetch addresses.

97. (Canceled)

98. (Currently Amended) The hardware component of Claim [[97]]89, wherein the counter component counts a number of times the received address matches the fixed instruction fetch address, as determined by the decoding component, and

wherein when the number reaches a predetermined threshold, the counter component provides a thread switch jump address to the second multiplexer and the decoding component sets the second multiplexer to provide the thread switch jump address as the output of the second multiplexer.

-- END OF AMENDMENT --

Reasons for Allowance

9. The following is an Examiner's statement of reasons for allowance:

The cited prior art taken alone or in combination fail to teach, in combination with the other claimed limitations, "incrementing an interpreter language program counter and sending a current interpretive language address to the memory component when the received address matches the fixed instruction fetch address; sending an operand address to the memory component when the received address matches one of the plurality of fixed operand fetch addresses; [and] sending the received address to the memory component when the received address fails to match the fixed instruction fetch address and any of the plurality of fixed operand fetch addresses" as recited in independent Claim 84; and further fail to teach, in combination with the other claimed limitations, "an instruction jump address generator component for receiving the data and providing inputs to the second multiplexer; an operand storing component

for receiving the data, storing any operands of the data, and providing inputs to the second multiplexer, wherein the decoding component controls the second multiplexer based on the result of the comparing; and a counter component for receiving an input from the decoding component and providing outputs to the second multiplexer and the decoding component” as recited in independent Claim 89.

The closest cited prior art, the combination of US 5,179,734 (hereinafter “Candy”), US 6,658,655 (hereinafter “Hoogerbrugge”), US 6,317,872 (hereinafter “Gee”), and US 5,586,256 (hereinafter “Thiel”), teaches a data processor that implements portions of a threaded interpretive language in hardware such that multiple software functions can be performed in a single clock cycle. However, the combination of Candy, Hoogerbrugge, Gee, and Thiel fails to teach “incrementing an interpreter language program counter and sending a current interpretive language address to the memory component when the received address matches the fixed instruction fetch address; sending an operand address to the memory component when the received address matches one of the plurality of fixed operand fetch addresses; [and] sending the received address to the memory component when the received address fails to match the fixed instruction fetch address and any of the plurality of fixed operand fetch addresses” as recited in independent Claim 84; and further fails to teach “an instruction jump address generator component for receiving the data and providing inputs to the second multiplexer; an operand storing component for receiving the data, storing any operands of the data, and providing inputs to the second multiplexer, wherein the decoding component controls the second multiplexer based on the result of the comparing; and a counter component for receiving an input from the

decoding component and providing outputs to the second multiplexer and the decoding component” as recited in independent Claim 89.

Any comments considered necessary by Applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

10. The prior art made of record and not relied upon is considered pertinent to Applicant’s disclosure.

11. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Qing Chen whose telephone number is 571-270-1071. The Examiner can normally be reached on Monday through Thursday from 7:30 AM to 4:00 PM. The Examiner can also be reached on alternate Fridays.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner’s supervisor, Wei Zhen, can be reached on 571-272-3708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the TC 2100 Group receptionist whose telephone number is 571-272-2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Q. C./

Examiner, Art Unit 2191

/Wei Y Zhen/

Supervisory Patent Examiner, Art Unit 2191